

【ABSTRACT】

A method of verifying a chip design includes: a software side operation step of transmitting output data generated by an operation of a software block to an interface unit, determining whether output data of a hardware block received via the interface unit is valid by executing a chip design verification program, and applying only valid output data of the hardware block to the software block; and a hardware side operation step of transmitting output data generated by an operation of the hardware block to the software block, determining whether the output data of the software block received is valid by executing the chip design verification program in the interface unit, and applying only valid output data of the software block to the hardware block.